

## Features

- Exceeds Requirements of EIA-485 Standard
- Hot Plug Circuitry - Tx and Rx Outputs Remain Three-State During Power-up/Power-down
- Supply Voltage: 3.0 V ~ 5.5 V
- Data Rate: 500-kbps Isolated RS485
- Full Fail-safe Receiver (Open, Short, Terminated)
- Up to 5-kV RMS Isolation Rating
- $\pm 200\text{-kV}/\mu\text{s}$  typ Static CMTI,  $\pm 150\text{-kV}/\mu\text{s}$  typ Dynamic CMTI
- Latch-up Performance Exceeds 600 mA
- Bus-Pin Protection (Between Bus pins and GNDB)
  - $\pm 15\text{-kV}$  IEC 61000-4-2 Contact Discharge
  - $\pm 20\text{-kV}$  IEC 61000-4-2 Air Discharge
- Safety-Related Certifications:
  - VDE Certification according to DIN VDE V 0884-17(IEC60747-17)
  - 5000-VRMS Isolation Rating per UL 1577
  - CQC Certification per GB 4943.1
  - CSA, TUV, and CB Certifications

## Applications

- Home Appliance
- Motor Drives
- Industrial Control
- Communication Infrastructure

## Description

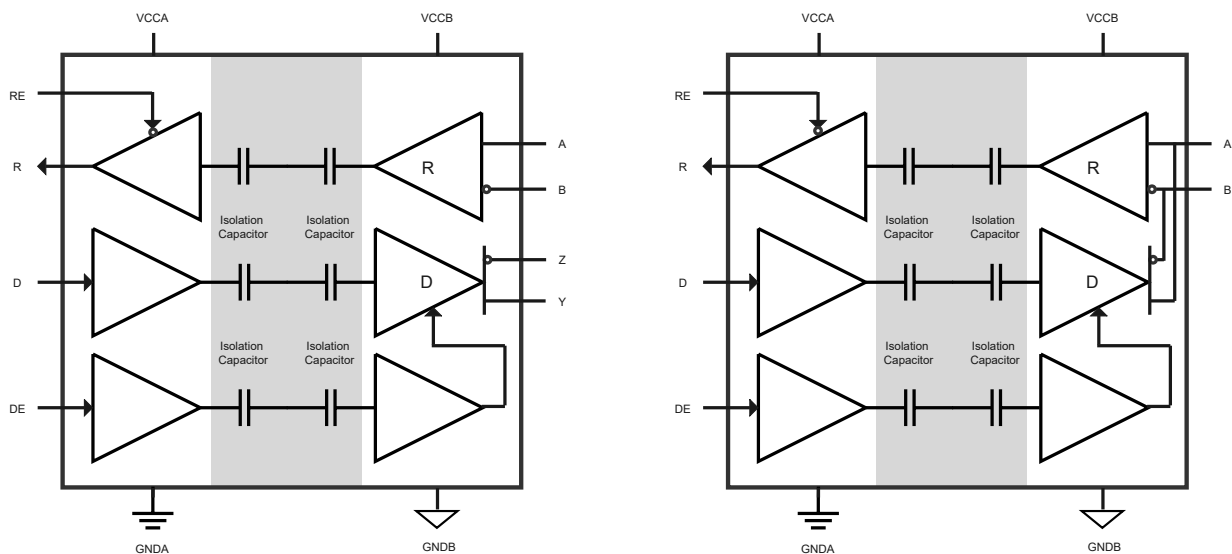
The TPT7487 is a  $\pm 15\text{-kV}$  IEC61000 ESD protected, 3.0-V ~ 5.5-V isolated RS485 transceivers that meet the RS-485 and RS-422 standards for Half Duplex communication. The TPT7488 is isolated Full Duplex RS485.

The TPT748x features a fail-safe receiver, which supports the output of the receiver to be logic high when the differential input (bus pin A/B) of the receiver is open, short, or idle.

Transmitters in this family deliver exceptional differential output voltages into the RS-485 required 54- $\Omega$  load. The 500-kbps device has very low bus currents, so they present a true “1/8 unit load” to the RS-485 bus. This allows up to 256 transceivers on the network without using repeaters. Receiver (Rx) inputs feature a “Full Fail-Safe” design, which ensures a logic-high Rx output if Rx inputs are floating, shorted, or on a terminated but undriven bus.

The TPT748x is available in the WSOP16 package and is characterized from  $-40^{\circ}\text{C}$  to  $+125^{\circ}\text{C}$ .

## Functional Block Diagram



## Table of Contents

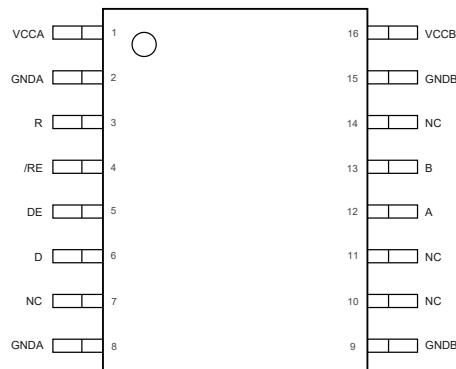
<b>Features</b> .....	<b>1</b>
<b>Applications</b> .....	<b>1</b>
<b>Description</b> .....	<b>1</b>
<b>Functional Block Diagram</b> .....	<b>1</b>
<b>Revision History</b> .....	<b>3</b>
<b>Pin Configuration and Functions</b> .....	<b>4</b>
<b>Specifications</b> .....	<b>6</b>
Absolute Maximum Ratings <sup>(1)</sup> .....	6
ESD, Electrostatic Discharge Protection.....	6
Recommended Operating Conditions.....	6
Thermal Information.....	7
Insulation Specifications.....	7
Safety-Related Certifications.....	9
Electrical Characteristics.....	10
Electrical Characteristics ( continued ) .....	11
Switching Characteristics.....	12
Test Circuits and Waveforms.....	13
<b>Detailed Description</b> .....	<b>15</b>
Overview.....	15
Functional Block Diagram.....	15
Feature Description.....	16
<b>Tape and Reel Information</b> .....	<b>17</b>
<b>Package Outline Dimensions</b> .....	<b>18</b>
WSOP16.....	18
<b>Order Information</b> .....	<b>19</b>
<b>IMPORTANT NOTICE AND DISCLAIMER</b> .....	<b>20</b>

## Revision History

Date	Revision	Notes
2022-03-10	Rev.Pre.0	Initial Version
2022-09-08	Rev.A.0	Released Version
2023-11-15	Rev.A.1	Updated new format and EC table

## Pin Configuration and Functions

TPT7487  
WSOP16  
Top View

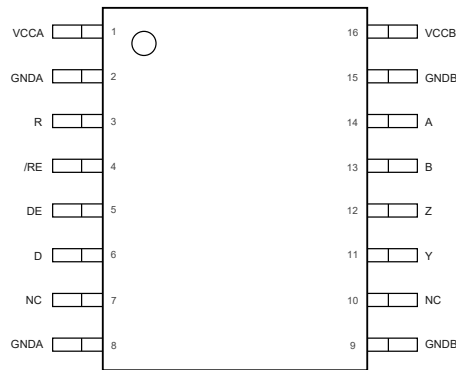


**Table 1. Pin Functions: TPT7487**

Pin No.	Name	I/O	Description
1	VCCA	–	Logic-side power supply
2	GNDA	–	Ground connection for VCCA
3	R	O	Receiver output
4	/RE	I	Receiver enable. This pin disables the receiver output when high or open and enables the receiver output when low.
5	DE	I	Driver enable. This pin enables the driver output when high and disables the driver output when low or open.
6	D	I	Driver input
7	NC	–	Not Connected
8	GNDA	–	Ground connection for VCCA
9	GNDB	–	Ground connection for VCCB
10	NC	–	Not Connected
11	NC	–	Not Connected
12	A	I/O	Transceiver non-inverting input or output (I/O) on the bus side
13	B	I/O	Transceiver inverting input or output (I/O) on the bus side
14	NC	–	Not Connected
15	GNDB	–	Ground connection for VCCB
16	VCCB	–	Transceiver-side power supply

**Isolated Full- and Half-Duplex RS485 Transceiver**

TPT7488  
WSOP16  
Top View



**Table 2. Pin Functions: TPT7488**

Pin No.	Name	I/O	Description
1	VCCA	—	Logic-side power supply
2	GNDA	—	Ground connection for VCCA
3	R	O	Receiver output
4	/RE	I	Receiver enable. This pin disables the receiver output when high or open and enables the receiver output when low.
5	DE	I	Driver enable. This pin enables the driver output when high and disables the driver output when low or open.
6	D	I	Driver input
7	NC	—	Not Connected
8	GNDA	—	Ground connection for VCCA
9	GNDB	—	Ground connection for VCCB
10	NC	—	Not Connected
11	Y	O	Driver non-inverting output
12	Z	O	Driver inverting output
13	B	I	Receiver inverting input on the bus side
14	A	I	Receiver non-inverting input on the bus side
15	GNDB	—	Ground connection for VCCB
16	VCCB	—	Transceiver-side power supply

## Specifications

### Absolute Maximum Ratings <sup>(1)</sup>

Parameter		Min	Max	Unit
V <sub>CC</sub>	Supply Voltage, V <sub>CCA</sub> , V <sub>CCB</sub>	-0.5	6	V
V <sub>IO</sub>	Voltage at Logic Pin: D, DE, /RE, R	-0.5	6	V
V <sub>BUS</sub>	Voltage at Bus Pin: A, B	-15	15	V
T <sub>A</sub>	Operating Temperature Range	-40	125	°C
T <sub>J</sub>	Maximum Junction Temperature	-40	150	°C
T <sub>S</sub>	Storage Temperature Range	-65	150	°C
T <sub>Lead</sub>	Lead Temperature (Soldering, 10 sec)	-	260	°C

(1) Stresses beyond those listed under Absolute Maximum Ratings may cause permanent damage to the device. Exposure to any Absolute Maximum Rating condition for extended periods may affect device reliability and lifetime.

(2) This data was taken with the JEDEC low effective thermal conductivity test board.

(3) This data was taken with the JEDEC standard multilayer test boards.

### ESD, Electrostatic Discharge Protection

Parameter		Condition	Value	Unit
IEC	IEC-61000-4-2, Contact Discharge	Bus pin to GNDB	±15	kV
	IEC-61000-4-2, Air-Gap Discharge	Bus pin to GNDB	±20	kV
HBM	HBM, per ANSI/ESDA/JEDEC JS-001 <sup>(1)</sup>	Bus pin to GNDB	±18	kV
		All Pins except bus pin	±7	kV
CDM	CDM, per ANSI/ESDA/JEDEC JS-002 <sup>(2)</sup>	All pins	±1.5	kV
LU	Latch up, per JESD78	All pins	±600	mA

(1) JEDEC document JEP155 states that 500-V HBM allows safe manufacturing with a standard ESD control process.

(2) JEDEC document JEP157 states that 250-V CDM allows safe manufacturing with a standard ESD control process.

### Recommended Operating Conditions

Parameter		Min	Max	Unit
V <sub>CC</sub>	Supply Voltage	3.0	5.5	V
V <sub>I</sub>	Input Voltage at any Bus Terminal	-7	12	V
V <sub>IH</sub>	High-level Input Voltage (driver, driver enable, and receiver enable inputs)	2	V <sub>CC</sub>	V
V <sub>IL</sub>	Low-level Input Voltage (driver, driver enable, and receiver enable inputs)	0	0.8	V
V <sub>ID</sub>	Differential Input Voltage	-7	12	V

**Isolated Full- and Half-Duplex RS485 Transceiver**

Parameter		Min	Max	Unit
R <sub>L</sub>	Differential Load Resistance	54	-	Ω

**Thermal Information**

Package Type	θ <sub>JA</sub>	θ <sub>JC</sub>	Unit
WSOP16	75	41	°C/W

**Insulation Specifications**

Parameter		Conditions	Value	Unit
			WSOP16	
CLR	External clearance	Shortest terminal-to-terminal distance through air	> 8.0	mm
CPG	External creepage	Shortest terminal-to-terminal distance across the package surface	> 8.0	mm
DTI	Distance through the insulation	Minimum internal gap (internal clearance)	> 22	μm
DTC	Distance through the Molding compound	Minimum internal distance across the conductors inside the package	0.8	mm
CTI	Comparative tracking index		> 600	V
	Material group		I	-
	Over-voltage category	For Rated Mains Voltage ≤ 150 V <sub>RMS</sub>	I-IV	-
		For Rated Mains Voltage ≤ 300 V <sub>RMS</sub>	I-IV	-
		For Rated Mains Voltage ≤ 600 V <sub>RMS</sub>	I-IV	-
		For Rated Mains Voltage ≤ 1000 V <sub>RMS</sub>	I-III	-
	Climatic category		40/125/21	-
	Pollution degree		2	-

**DIN V VDE V 0884-17 <sup>(1)(2)</sup>**

V <sub>IORM</sub>	Maximum repetitive isolation voltage	AC voltage	1414	V <sub>PK</sub>
V <sub>IOWM</sub>	Maximum working isolation voltage	AC voltage; TDDB Test	1000	V <sub>RMS</sub>
		DC voltage	1414	V <sub>DC</sub>
V <sub>IOTM</sub>	Maximum transient isolation voltage	V <sub>TEST</sub> = V <sub>IOTM</sub> , t = 60 s (qualification); V <sub>TEST</sub> = 1.2 × V <sub>IOTM</sub> , t = 1 s (100% production)	7070	V <sub>PK</sub>
V <sub>IOSM</sub>	Maximum surge isolation voltage <sup>(3)</sup>	Test method per IEC 62368-1, 1.2/50 μs waveform, V <sub>TEST</sub> = 1.3 × V <sub>IOSM</sub> (qualification)	6500	V <sub>PK</sub>
q <sub>pd</sub>	Apparent charge	Method a, After Input/Output safety test subgroup 2/3, V <sub>ini</sub> = V <sub>IOTM</sub> , t <sub>ini</sub> = 60 s; V <sub>pd(m)</sub> = 1.2 × V <sub>IORM</sub> , t <sub>m</sub> = 10 s	≤ 5	pC

**Isolated Full- and Half-Duplex RS485 Transceiver**

Parameter		Conditions	Value	Unit
			WSOP16	
		Method a, After environmental tests subgroup 1, $V_{ini} = V_{IOTM}$ , $t_{ini} = 60$ s; $V_{pd(m)} = 1.6 \times V_{IORM}$ , $t_m = 10$ s	$\leq 5$	
		Method b1; At routine test (100% production) and preconditioning (type test), $V_{ini} = 1.2 \times V_{IOTM}$ , $t_{ini} = 1$ s; $V_{pd(m)} = 1.875 \times V_{IORM}$ , $t_m = 1$ s	$\leq 5$	
$C_{IO}$	Isolation capacitance	$V_{IO} = 0.4 \times \sin(2\pi ft)$ , $f = 1$ MHz	$\sim 0.5$	pF
$R_{IO}$	Isolation resistance	$V_{IO} = 500$ V, $T_A = 25^\circ\text{C}$	$> 10^{12}$	$\Omega$
		$V_{IO} = 500$ V, $100^\circ\text{C} \leq T_A \leq 125^\circ\text{C}$	$> 10^{11}$	$\Omega$
		$V_{IO} = 500$ V at $T_S = 150^\circ\text{C}$	$> 10^9$	$\Omega$
<b>UL 1577</b>				
$V_{ISO}$	Withstanding isolation voltage	$V_{TEST} = V_{ISO}$ , $t = 60$ s (qualification); $V_{TEST} = 1.2 \times V_{ISO}$ , $t = 1$ s (100% production)	5000	$V_{RMS}$

- (1) All pins on each side of the barrier are tied together creating a two-terminal device.
- (2) This coupler is suitable for safe electrical insulation only within the safety operating ratings. Compliance with the safety ratings shall be ensured by means of suitable protective circuits.
- (3) Testing must be carried out in oil.



**Isolated Full- and Half-Duplex RS485 Transceiver**
**Safety-Related Certifications**

VDE	UL	TUV	CQC	CSA	CB
Certified according to DIN VDE V 0884-17	Certified according to UL 1577 and CSA Component Acceptance Notice 5A	Certified according to EN IEC 62368-1 and EN IEC 61010-1	Certified according to GB 4943.1	Certified CSA C22.2 No. 62368-1 and CAN/CSA-C22.2 No. 60601-1	Certified according to EN IEC 62368-1
Basic insulation (WSOP) VIORM= 1414 VIOSM= 6500 (SOP, QSOP) VIORM= 637 VIOSM= 5980	(WSOP)Single protection, 5000Vrms (SOP, QSOP)Single protection, 3750Vrms	6400Vrms reinforced insulation (WSOP), 800Vrms maximum work voltage 4000Vrms basic insulation (SOP, QSOP), 400V rms maximum work voltage	Reinforced insulation (WSOP), Altitude<=5000m, 800V rms maximum work voltage. Basic insulation (SOP, QSOP), Altitude<=5000m, 400V rms maximum work voltage.	400Vrms basic insulation (SOP, QSOP) and 600V rms reinforced insulation (WSOP) working voltage per CSA C22.2 No. 62368-1:19 3rd, IEC 62368-1:2018 Ed. 3(in pollution degree 2, material group I) 2 MOPP (Means of Patient Protection) insulation requirements for 250Vrms (WSOP) in CAN/CSA-C22.2 No. 60601-1:14, IEC 60601-1:2005 + AMD1:2012	Reinforced insulation (WSOP), Altitude<=5000m, 800V rms maximum work voltage. Basic insulation (SOP, QSOP), Altitude<=5000m, 400V rms maximum work voltage.
Certificate No. 40054570	Report Reference E524241	Customer Number : 2332359	Certificate No. CQC22001356658	Master contract: 302375	Ref. Certif. No. CN57658

**Electrical Characteristics**

All test conditions:  $V_{CCA} = 2.25\text{ V to }5.5\text{ V}$ ,  $V_{CCB} = 3\text{ V to }5.5\text{ V}$ ,  $T_A = -40^\circ\text{C to }125^\circ\text{C}$ , unless otherwise noted.

Parameter		Test Conditions	Min	Typ	Max	Unit
V <sub>Od</sub>	Driver Differential Output Voltage Magnitude	R <sub>L</sub> = 54 Ω, V <sub>CCB</sub> = 3.3 V	1.5	2.2	-	V
		R <sub>L</sub> = 54 Ω, V <sub>CCB</sub> = 5.0 V	2.0	3.3	-	V
		R <sub>L</sub> = 100 Ω, V <sub>CCB</sub> = 3.3 V	1.5	2.6	-	V
		R <sub>L</sub> = 100 Ω, V <sub>CCB</sub> = 5.0 V	2.0	3.9	-	V
Δ V <sub>Od</sub>	Change in Magnitude of Driver Differential Output Voltage		-200	-	200	mV
V <sub>Oc(ss)</sub>	Steady-state Common-mode Output Voltage	Center of two 27-Ω load resistors	1	V <sub>CC</sub> /2	3	V
ΔV <sub>Oc</sub>	Change in Differential Driver Output Common-mode Voltage		-200	-	200	mV
C <sub>OD</sub>	Differential Output Capacitance <sup>(1)</sup>		-	15	-	pF
V <sub>IT+</sub>	Positive-going Receiver Differential Input Voltage Threshold		-	-110	-50	mV
V <sub>IT-</sub>	Negative-going Receiver Differential Input Voltage Threshold		-200	-130	-	mV
V <sub>HYS</sub>	Receiver Differential Input Voltage threshold Hysteresis (V <sub>IT+</sub> – V <sub>IT-</sub> ) <sup>(1)</sup>		-	50	-	mV
V <sub>OH</sub>	Receiver High-level Output Voltage	V <sub>CCA</sub> = 3.3 V, I <sub>OH</sub> = -2 mA	2.7	3.2	-	V
		V <sub>CCA</sub> = 5 V, I <sub>OH</sub> = -4 mA	4.1	4.8	-	
V <sub>OL</sub>	Receiver Low-level Output Voltage	V <sub>CCA</sub> = 3.3 V, I <sub>OL</sub> = 2 mA	-	0.09	0.3	V
		V <sub>CCA</sub> = 5 V, I <sub>OL</sub> = 4 mA	-	0.17	0.4	
V <sub>IH</sub>	Input High Logic Level	D, DE, /RE	2.0	-	-	V
V <sub>IL</sub>	Input Low Logic Level	D, DE, /RE	-	-	0.8	V
I <sub>IN</sub>	Driver Input, driver enable, and receiver enable input current <sup>(1)</sup>	D, DE, /RE	-	3	-	μA
I <sub>OZ</sub>	Driver Output high-Z current	V <sub>O</sub> = -7 V	-100	-	-	μA
		V <sub>O</sub> = 12 V		-	125	μA
I <sub>OZ</sub>	Receiver high-Z current <sup>(1)</sup>	V <sub>O</sub> = 0 V or V <sub>CCA</sub>	-	0.1	-	μA
I <sub>OS</sub>	Driver Short-circuit Output Current	V <sub>DE</sub> = V <sub>CCA</sub> , V <sub>CCB</sub> = 3.0 ~ 5.5 V	-250	-	250	mA

(1) Parameters are provided by lab bench test and design simulation.

**Isolated Full- and Half-Duplex RS485 Transceiver**
**Electrical Characteristics ( continued )**

 All test conditions:  $V_{CCA} = 2.25\text{ V to }5.5\text{ V}$ ,  $V_{CCB} = 3\text{ V to }5.5\text{ V}$ ,  $T_A = -40^\circ\text{C to }125^\circ\text{C}$ , unless otherwise noted.

Parameter		Test Conditions		Min	Typ	Max	Unit
$I_{IA/B}$	Bus input current (disabled driver)	DE = 0 V, /RE = $V_{CCA}$	$V_I = 12\text{ V}$ ,	-	55	125	$\mu\text{A}$
			$V_I = -7\text{ V}$ ,	-100	-50	-	$\mu\text{A}$
$I_{CCA}$	Supply current, DC	Driver and Receiver enabled	$V_{CCA} = 5\text{ V}$ , No load	-	6.9	9.8	mA
			$V_{CCA} = 3.3\text{ V}$ , No load	-	6.8	9.8	mA
		Driver enable and Receiver disabled	$V_{CCA} = 5\text{ V}$ , No load	-	6.9	9.8	mA
			$V_{CCA} = 3.3\text{ V}$ , No load	-	6.8	9.8	mA
		Driver disable and Receiver enabled	$V_{CCA} = 5\text{ V}$ , No load	-	3.4	5	mA
			$V_{CCA} = 3.3\text{ V}$ , No load	-	3.4	5	mA
		Driver and receiver disabled	$V_{CCA} = 5\text{ V}$ , No load	-	3.4	5	mA
			$V_{CCA} = 3.3\text{ V}$ , No load	-	3.4	5	mA
$I_{CCA}$	Supply current, 500kbps	Driver and Receiver enabled	$V_{CCA} = 5\text{ V}$ , No load	-	9	14	mA
			$V_{CCA} = 3.3\text{ V}$ , No load	-	8.9	14	mA
		Driver enable and Receiver disabled	$V_{CCA} = 5\text{ V}$ , No load	-	8.9	14	mA
			$V_{CCA} = 3.3\text{ V}$ , No load	-	8.9	14	mA
		Driver disable and Receiver enabled	$V_{CCA} = 5\text{ V}$ , No load	-	3.4	6.8	mA
			$V_{CCA} = 3.3\text{ V}$ , No load	-	3.3	6.8	mA
$I_{CCB}$	Supply current, DC	Driver enabled	$V_{CCB} = 5\text{ V}$ , No load	-	3.9	6.5	mA
			$V_{CCB} = 3.3\text{ V}$ , No load	-	3.8	6.3	mA
		Driver and Receiver enabled	$V_{CCB} = 5\text{ V}$ , RL=54 $\Omega$	-	73	93	mA
			$V_{CCB} = 3.3\text{ V}$ , RL=54 $\Omega$	-	48	61	mA
		Driver disabled	$V_{CCB} = 5\text{ V}$ , No load	-	3.9	6.3	mA
			$V_{CCB} = 3.3\text{ V}$ , No load	-	3.8	6.2	mA
$I_{CCB}$	Supply current, 500kbps	Driver and Receiver enabled	$V_{CCB} = 5\text{ V}$ , RL=54 $\Omega$	-	108.8	160	mA
			$V_{CCB} = 3.3\text{ V}$ , RL=54 $\Omega$	-	57.5	95	mA
CMTI	Common-mode transient immunity	$V_{CM} = 1200\text{ V}$	-	100	150	-	kV/ $\mu\text{s}$

(1) Parameters are provided by lab bench test and design simulation.

**Switching Characteristics**

All test conditions:  $V_{CCA} = 2.25\text{ V TO } 5.5\text{ V}$ ,  $V_{CCB} = 3\text{ V to } 5.5\text{ V}$ ,  $T_A = -40^\circ\text{C to } 125^\circ\text{C}$ , 500Kbps, unless otherwise noted.

Parameter	Conditions	Min	Typ	Max	Units		
<b>Driver</b>							
$t_r, t_f$	Driver Differential-Output Rise and fall times <sup>(1)</sup>	-	470	700	ns		
$t_{PHL}, t_{PLH}$	Driver Propagation Delay	-	460	700			
$t_{SK(P)}$	Driver Pulse Skew, $ t_{PHL} - t_{PLH} $ <sup>(1)</sup>	-	6				
$t_{PHZ}, t_{PLZ}$	Driver disable time	/RE = 0 or $V_{CCA}$		-	75	200	ns
$t_{PZH}, t_{PZL}$	Driver Enable Time	-	350	1000	ns		
<b>Receiver</b>							
$t_r, t_f$	Driver Differential-Output Rise and Fall Times <sup>(1)</sup>	-	2	4	ns		
$t_{PHL}, t_{PLH}$	Receiver Propagation Delay Time	$C_L = 15\text{ pF}$		-	115	220	ns
$t_{SK(P)}$	Receiver Pulse Skew, $ t_{PHL} - t_{PLH} $ <sup>(1)</sup>	-	5				
$t_{PHZ}, t_{PLZ}$	Receiver Disable Time	DE = 0 or $V_{CCA}$		-	35	60	ns
$t_{PZH}, t_{PZL}$	Receiver Enable Time	-	30	60	ns		

(1) Parameters are provided by lab bench test and design simulation.

Test Circuits and Waveforms

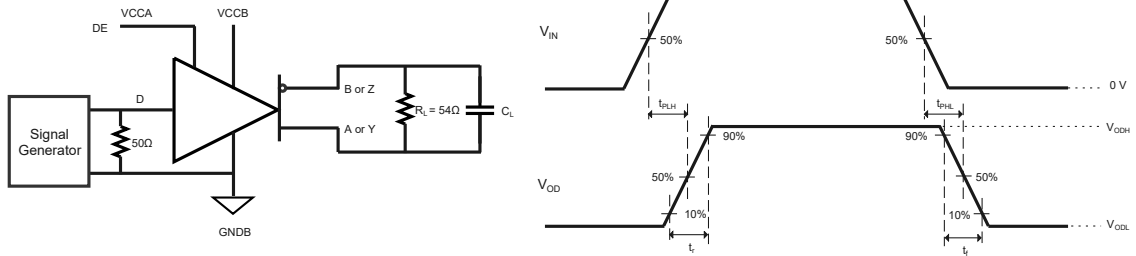


Figure 1. Driver Switching Test Circuit and Waveforms

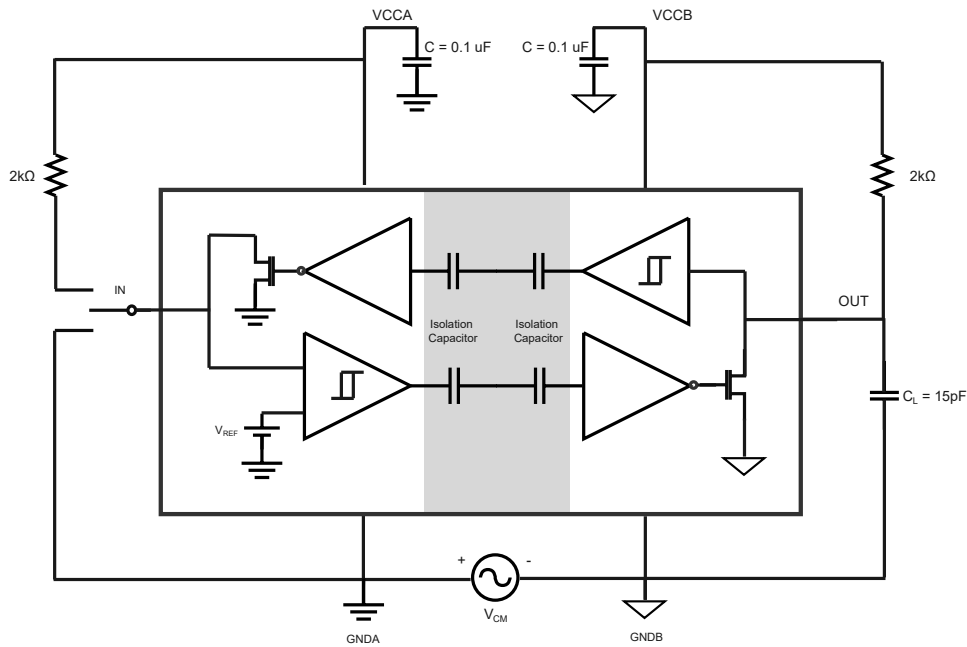


Figure 2. Common Mode Transient Immunity Test Circuit

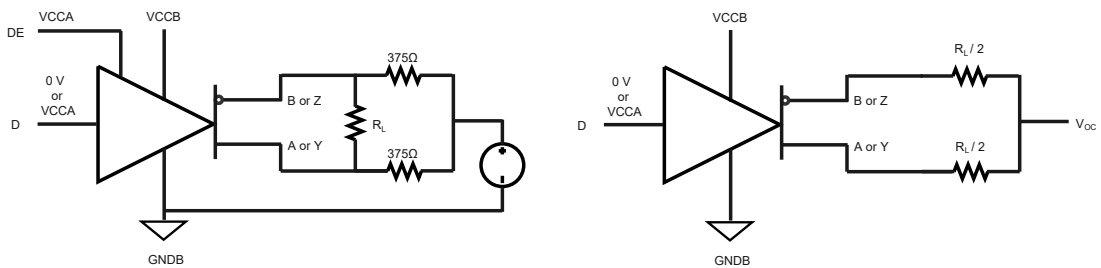


Figure 3. Driver Test Circuit

Isolated Full- and Half-Duplex RS485 Transceiver

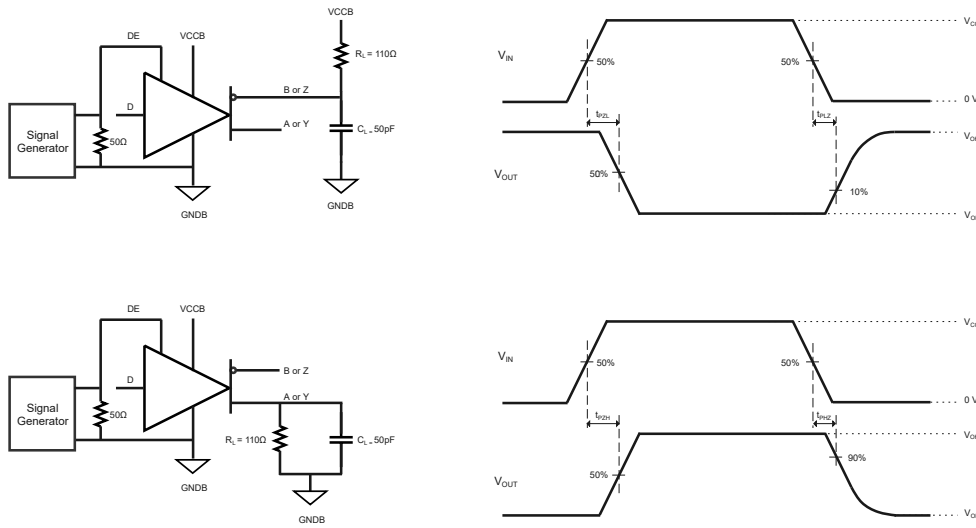


Figure 4. Driver Enable: Disable Propagation Delay Test Circuit and Waveforms

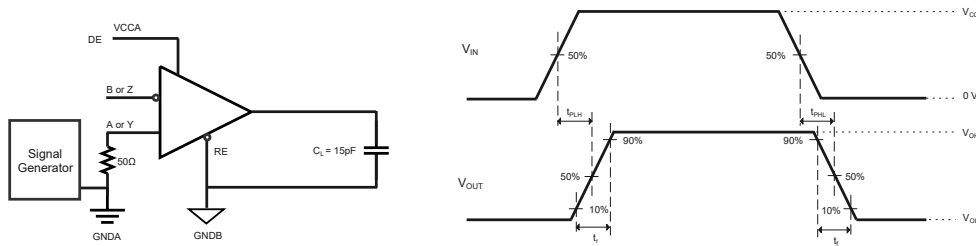


Figure 5. Receiver Switching Test Circuit and Waveforms

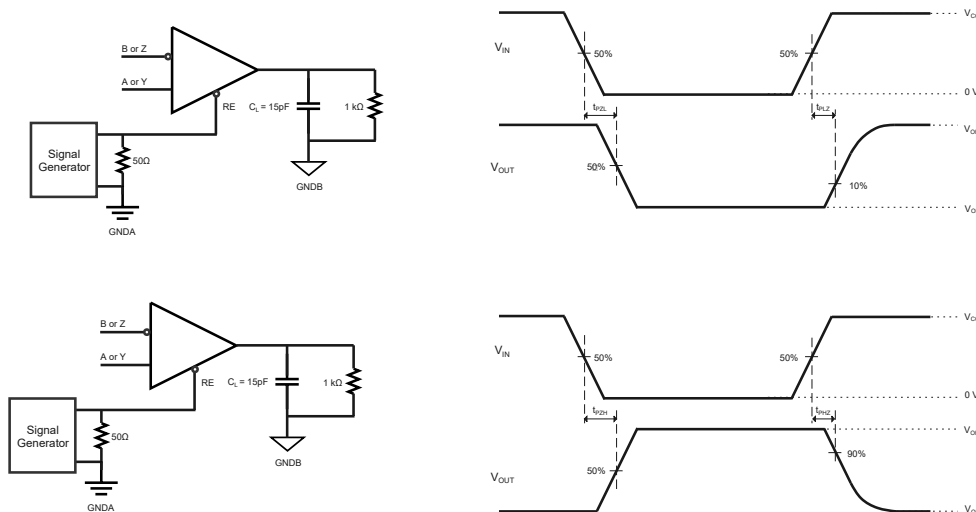


Figure 6. Receiver Enable: Disable Propagation Delay Test Circuit and Waveforms

## Isolated Full- and Half-Duplex RS485 Transceiver

### Detailed Description

#### Overview

The TPT748x features a fail-safe receiver, which supports the output of the receiver to be logic high when the differential input (bus pin A/B) of the receiver is open, short, or idle when  $RXP = 0\text{ V}$ .

Transmitters in this family deliver exceptional differential output voltages into the RS-485 required  $54\text{-}\Omega$  load. The 500-kbps device has very low bus currents, so they present a true “1/8 unit load” to the RS-485 bus. This allows up to 256 transceivers on the network without using repeaters. Receiver (Rx) inputs feature a “Full Fail-Safe” design, which ensures a logic-high Rx output if Rx inputs are floating, shorted, or on a terminated but undriven bus.

#### Functional Block Diagram

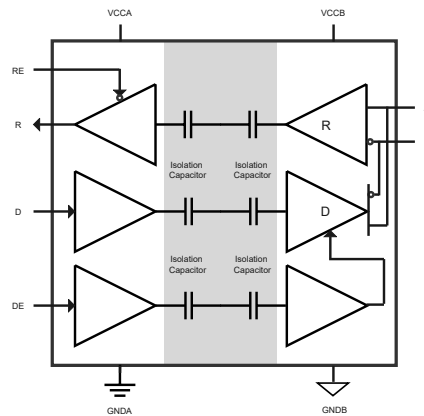


Figure 7. TPT7487 Block Diagram

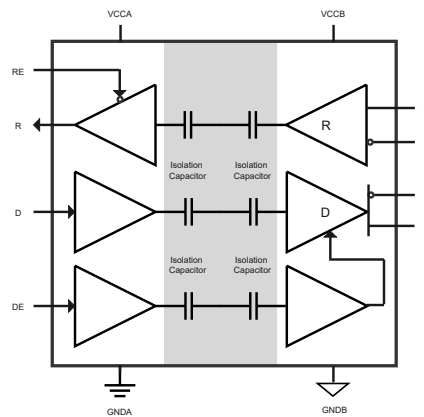


Figure 8. TPT7488 Block Diagram

**Feature Description**
**Table 3. Driver Function Table**

Input	Enable	Output	Output	Description
D	DE	A	B	
H	H	H	L	Actively drives bus High
L	H	L	H	Actively drives bus Low
X	L	Z	Z	Driver disabled
X	OPEN	Z	Z	Driver disabled by default
Open	H	H	L	Actively drives bus High by default

(1) X = don't care

(2) Z = high impedance

**Table 4. Receiver Function Table**

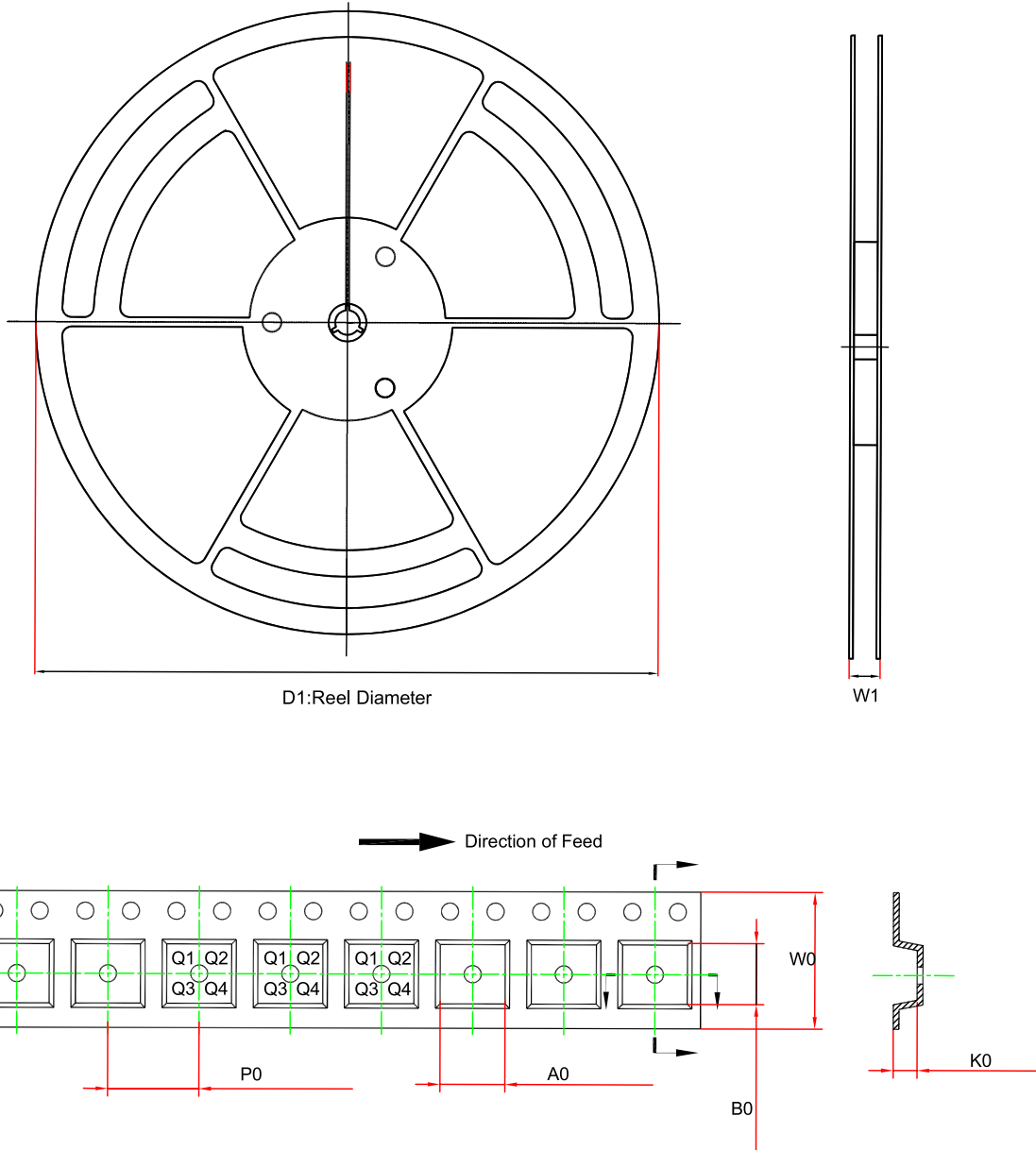
Input	Input	Output	Description
A-B	/RE	R	
> -50mV	L	H	Receive valid bus High
-200 mV < Input < -50 mV	L	?	Indeterminate bus state
< -200 mV	L	L	Receive valid bus Low
X	H	Z	Receiver disabled
X	OPEN	Z	Receiver disabled in default
Open	L	H	Fail-safe high output
Short	L	H	Fail-safe high output
Idle (Terminated)	L	H	Fail-safe high output

(1) X = don't care

(2) Z = high impedance



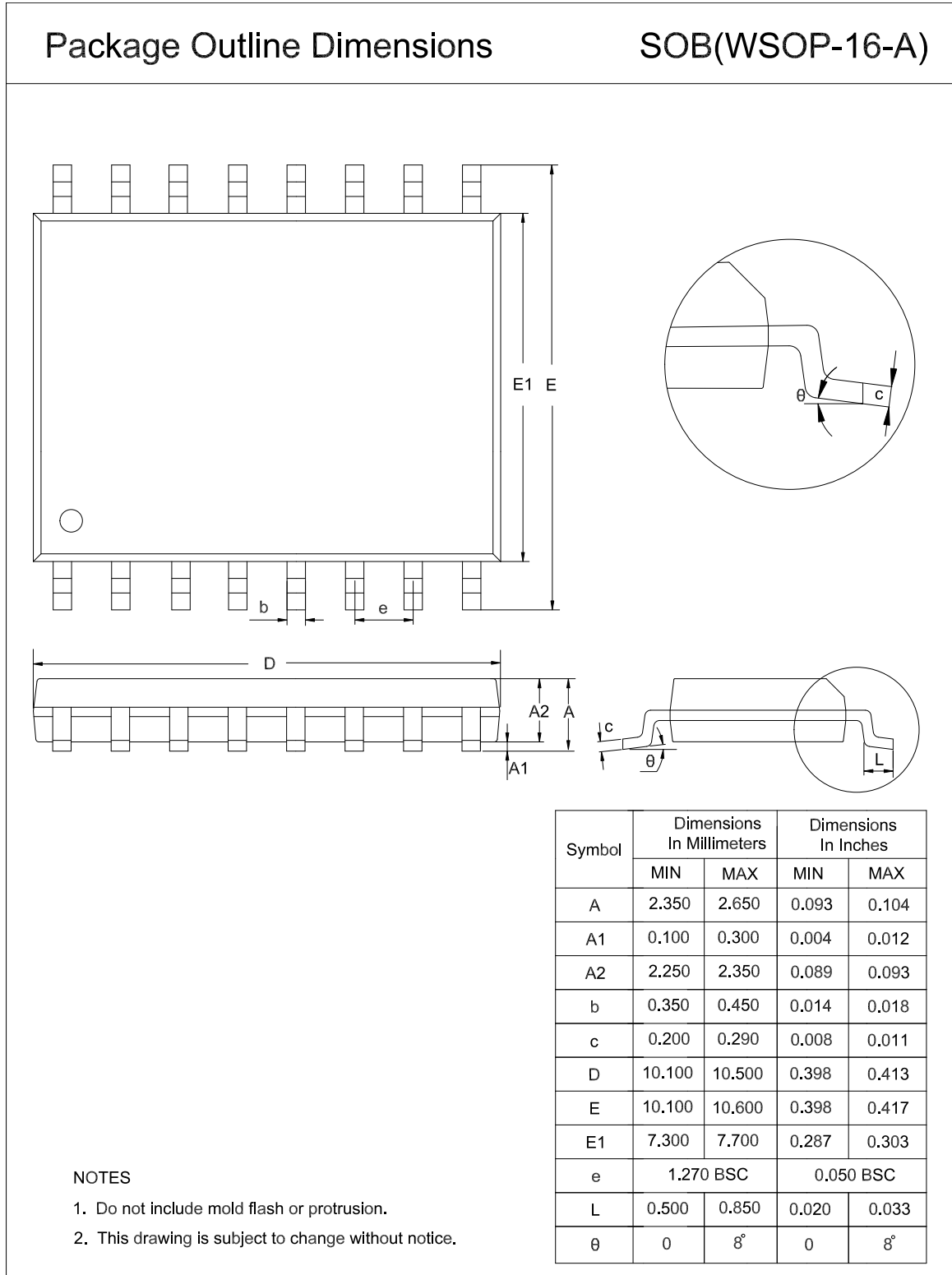
### Tape and Reel Information



Order Number	Package	D1 (mm)	W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P0 (mm)	W0 (mm)	Pin1 Quadrant
TPT7487-SOBR	WSOP16	330	21.6	10.9	10.8	3.0	12.0	16.0	Q1
TPT7488-SOBR	WSOP16	330	21.6	10.9	10.8	3.0	12.0	16.0	Q1

Package Outline Dimensions

WSOP16



## Order Information

Order Number	Operating Temperature Range	Package	Marking Information	MSL	Transport Media, Quantity	Eco Plan
TPT7487-SOBR	-40 to 125°C	WSOP16	T7487	MSL3	Tape and Reel, 1500	Green
TPT7488-SOBR	-40 to 125°C	WSOP16	T7488	MSL3	Tape and Reel, 1500	Green

**Green:** 3PEAK defines "Green" to mean RoHS compatible and free of halogen substances.

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