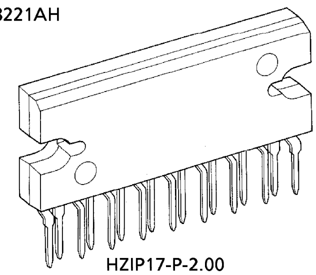
TA8221AH, TA8221AL

SILICON MONOLITHIC

30W BTL×2CH AUDIO POWER AMPLIFIER

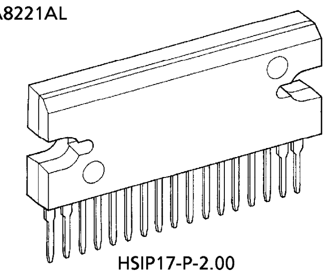
The thermal resistance θj-T of TA8221AH, TA8221AL package designed for low thermal resistance, has a high efficiency of heat radiation.
The temperature rise of chip can be reduced and the influence from the degradation of the features due to the temperature rise at the high output can also be reduced.
This stereo audio power IC, designed for car audio use, has two built-in channels to reduce the characteristic difference between L and R channels.
It also contains various kind of protection.

TA8221AH



HZIP17-P-2.00

TA8221AL



HSIP17-P-2.00

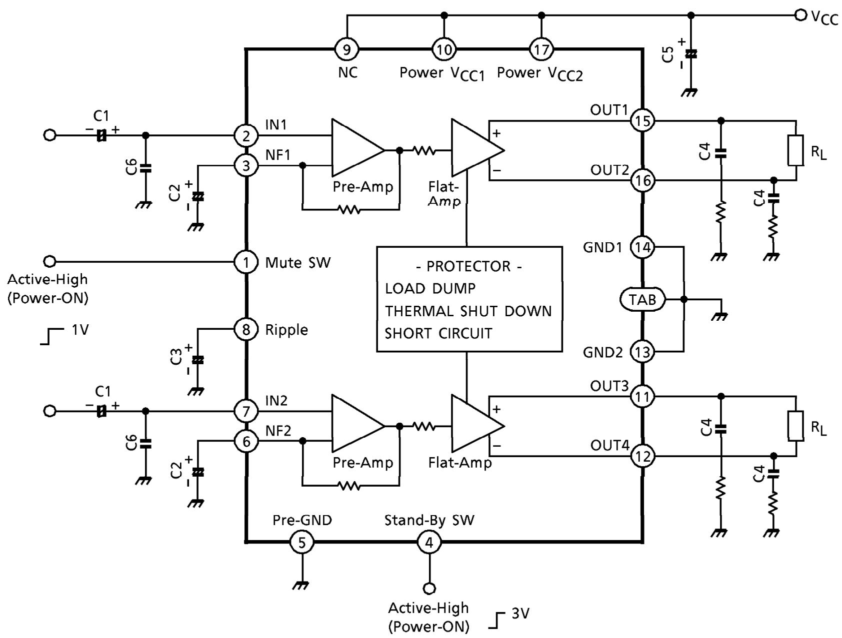
Weight
HZIP17-P-2.00 : 9.8g (Typ.)
HSIP17-P-2.00 : 9.8g (Typ.)

FEATURES

- Low Thermal Resistance
  : θj-T = 1.5°C / W (Infinite Heat Sink)
- High Power
  : $P_{OUT\,(1)} = 30W$ (Typ.) / Channel
  ($V_{CC} = 14.4V$, f = 1kHz, THD = 10%, $R_L = 2\Omega$)
  $P_{OUT\,(2)} = 26W$ (Typ.) / Channel
  ($V_{CC} = 13.2V$, f = 1kHz, THD = 10%, $R_L = 2\Omega$)
  $P_{OUT\,(3)} = 19W$ (Typ.) / Channel
  ($V_{CC} = 13.2V$, f = 1kHz, THD = 10%, $R_L = 4\Omega$)
- Low Distortion Ratio : THD = 0.04% (Typ.) ($V_{CC} = 13.2V$, f = 1kHz, $P_{OUT} = 1W$, $R_L = 4\Omega$, $G_V = 50dB$)
- Low Noise : $V_{NO} = 0.30mV_{rms}$ (Typ.) ($V_{CC} = 13.2V$, $R_L = 4\Omega$, $G_V = 50dB$, $R_g = 0\Omega$, BW = 20Hz~20kHz)
- Built-in Stand-by Function (With pin④ set at LOW, power is turned OFF.) : $I_{SB} = 100\mu A$ (Typ.)
- Built-in Muting Function (With pin① set at LOW, power is turned OFF.)
- Built-in Various Protection Circuits
  Protection Circuits : Thermal Shut Down, Over Voltage, Out→V_{CC} Short, Out→GND Short and OUT-OUT Short.
- Operating Supply Voltage : $V_{CC\,(opr)} = 9\sim18V$

BLOCK DIAGRAM

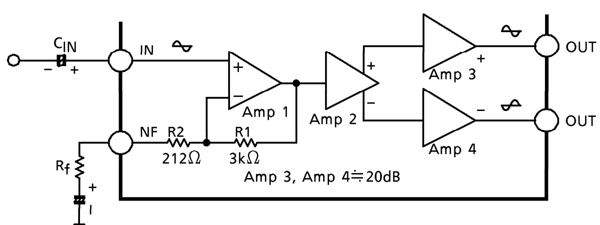
TA8221AH, TA8221AL ($G_V = 50dB$)



CAUTION AND APPLICATION METHOD

(Description is made only on the single channel.)

1. Voltage gain adjustment



Amp 1 : Pre-Amp
Amp 2 : Phase Amp
Amp 3 : POWER Amp (Flat-Amp)
Amp 4 : POWER Amp (Flat-Amp)

Fig.1

This IC has the amplifier constructions as shown Fig.1. The Pre-Amp (Amp 1) is provided to the primary stage, and the input voltage is amplified by the Flat Amps, Amp 3 and Amp 4 of each channel through the phase Amp (Amp 2).
Since the input offset is prevented by Pre-Amp when V_{CC} is set to ON, this circuit can remarkably reduce the pop noise.

The total closed loop gain G_V of this IC can be obtained by expression below when the closed loop voltage gain of Amp 1 is G_{V1}.

$$G_{V1} = 20\ell og \frac{R1 + (R_f + R2)}{R_f + R2} \text{ (dB)} \quad \ldots\ldots \quad (1)$$

The closed loop voltage gain of POWER Amp, Amp 3 and Amp 4 is fixed at $G_{V3} \doteqdot G_{V4} = 20\text{dB}$.
Therefore, the total closed circuit voltage gain G_V is obtained through BTL connection by the expression below.

$$G_V = G_{V1} + G_{V3} + 6 \text{ (dB)} \quad \ldots\ldots\ldots\ldots\ldots \quad (2)$$

For example, when $R_f = 0\Omega$, G_V is obtained by the expressions (1) and (2) as shown below.

$$G_V \doteqdot 24 + 20 + 6 = 50\text{dB}$$

The voltage gain is reduced when R_f is increased. (Fig.2) With the voltage gain reduced, since (1) the oscillation stability is reduced, and (2) the pop noise changes when V_{CC} is set to ON, refer to the items 3 and 4.

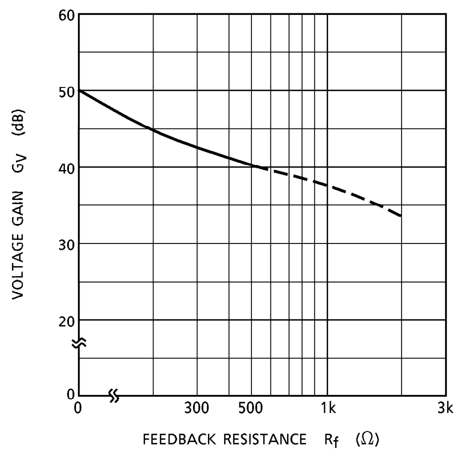


Fig.2

2. Stand-by SW function

By means of controlling pin④ (Stand-by terminal) to High and Low, the power supply can be set to ON and OFF. The threshold voltage of pin④ is set at 2.1V ($3V_{BE}$), and the power supply current is about 100μA (Typ.) at the stand-by state.

Control voltage of pin④ : $V_{(SB)}$

STAND-BY	POWER	$V_{(SB)}$ (V)
ON	OFF	0~2
OFF	ON	3~V_{CC}

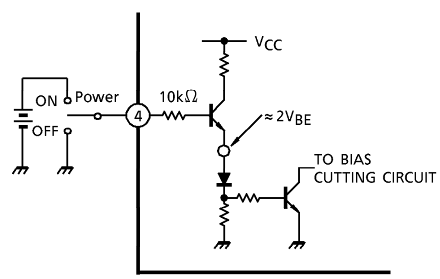
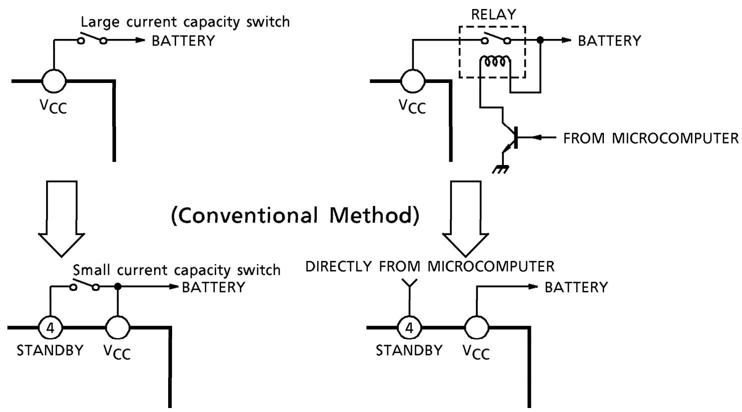


Fig.3 With pin④ set to High, Power is turned ON.

Adjustage of stand-by SW

(1) Since V_{CC} can directly be controlled to ON / OFF by the microcomputer, the switching relay can be omitted.

(2) Since the control current is microscopic, the switching relay of small current capacity is satisfactory for switching.



(Standby Switch Method)

3. Preventive measure against oscillation

For preventing the oscillation, it is advisable to use C4, the condenser of polyester film having small characteristic fluctuation of the temperature and the frequency.
The condenser (C6) between input and GND is effective for preventing oscillation which is generated with a feedback signal from an output stage.
The resistance R to be series applied to C4 is effective for phase correction of high frequency, and improves the oscillation allowance.

(1) Voltage gain to be used (G_V Setting)
(2) Capacity value of condenser
(3) Kind of condenser
(4) Layout of printed board

In case of its use with the voltage gain G_V reduced or with the feedback amount increased, care must be taken because the phase-inversion is caused by the high frequency resulting in making the oscillation liable generated.

4. Adjustment of output offset (When the power supply turn on)

As this IC is contructed with DC circuit on the primary stage, it is necessary to lower a input offset or output offset by agreement with the each leading edge time constant of the input voltage in the primary stage and NF terminal voltage.
Concretely, monitor the output DC voltage and vary the capacity value in input condenser and NF condenser (See Fig.4)

(Reference) In case of setting the condition ($G_V = 40dB$) with $R_f = 470\Omega$.

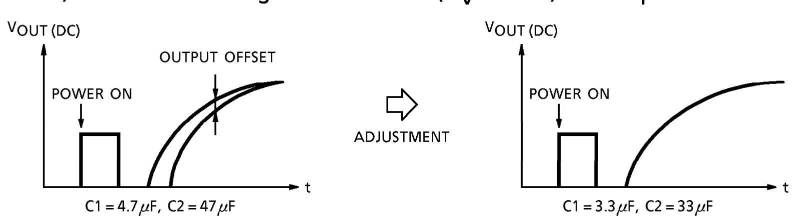


Fig.4

5. Muting function

Through setting pin① (mute terminal) at about 1V or less, muting becomes possible.
The interval circuit of IC is shown in Fig.5.
When pin① is set to LOW, Q1 and Q2 are turned to ON, the charge of the ripple condenser is discharged and the bias is cut. The mute amount of 60dB or over can be obtained.
Since this muting function rapidly discharge the charge of the ripple filter capacitor of pin⑧, the pop noise is generated by the DC fluctuation of the bias section.
Therefore, this muting function is not appropriate to the audio muting but it is effective in muting at V_{CC}→ON.

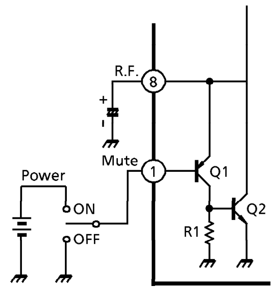


Fig.5 Mute circuit

6. Rapid ripple discharging circuit at the time of V_{CC} OFF

This circuit is effective in such a mode where the V_{CC} and the Stand-by terminals become high / low simultaneously ; for instance, for a pop noise produced when the power is turned ON / OFF repeatedly by operating the ignition key.

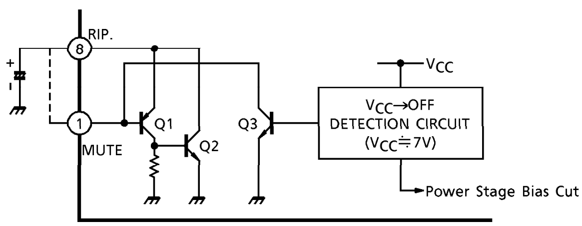


Fig.6

If V_{CC} is OFF, V_{CC}≒7V is detected internally on IC and

(1) The power stage bias circuit is cut, and
(2) Pin⑧ : Ripple Capacitor is rapidly discharged by turning Q3 ON and then Q1 and Q2 ON.

(Precaution 1)

When the stand-by terminal was put to the low level after the ripple rapid discharging circuit was operated (V_{CC}≒7V) at the time when V_{CC} was turned OFF, a pop noise may be generated. Therefore, V_{CC} which makes the Stand-by terminal low shall be set at 8V or above so that (1) the Stand-by terminal is put at the low level and (2) the ripple rapid discharging circuit is turned ON when V_{CC} is turned OFF (in order of (1) and (2)).
An example of application is shown in (Fig.7).

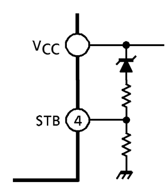


Fig.7

(Precaution 2)

If the falling time constant of the V_{CC} line is large (the fall is gentle), the pop noise may become worse.
In this case, it is possible to prevent the pop noise from becoming worse by reducing the capacity of "Ripple Rapid Discharging Circuit at the time of V_{CC} OFF" according to the following steps :

(a) Short pin① (Mute Terminal) and pin⑧ (Ripple Terminal).
(b) Increase the capacity of ripple capacitor of pin⑧.

However, it shall be kept in mind that the time for turning the power ON becomes longer as the result of step (b).

7. External part list and description

SYMBOL	RECOMMENDED VALUE	FEATURE	INFLUENCE		REMARKS
			SMALLER THAN RECOMMENDED VALUE	LARGER THAN RECOMMENDED VALUE	
C1	4.7μF	DC blocking	Related to pop noise at V_{CC}→ON.		Related to gain. Refer to item 4.
C2	47μF	Feedback condenser	Related to pop noise at V_{CC}→ON. Determination of low cut-off frequency. $C2 = \frac{1}{2\pi \cdot f_L \cdot R_f}$		
C3	220μF	Ripple reduction	Time constant is small at V_{CC}→ON or OFF.	Time constant is large at V_{CC}→ON or OFF.	
C4	0.12μF	Oscillation prevention	Made liable to oscillate.	Oscillation allowance.	Refer to item 3.
C5	1000μF	Ripple filter	For filtering power supply hum and ripple. Large at using AC rectified power supply. Small at using DC power supply.		
C6	1000pF	Oscillation prevention	Oscillation allowance improved. Noise reduction		Refer to item 3.

MAXIMUM RATINGS (Ta = 25°C)

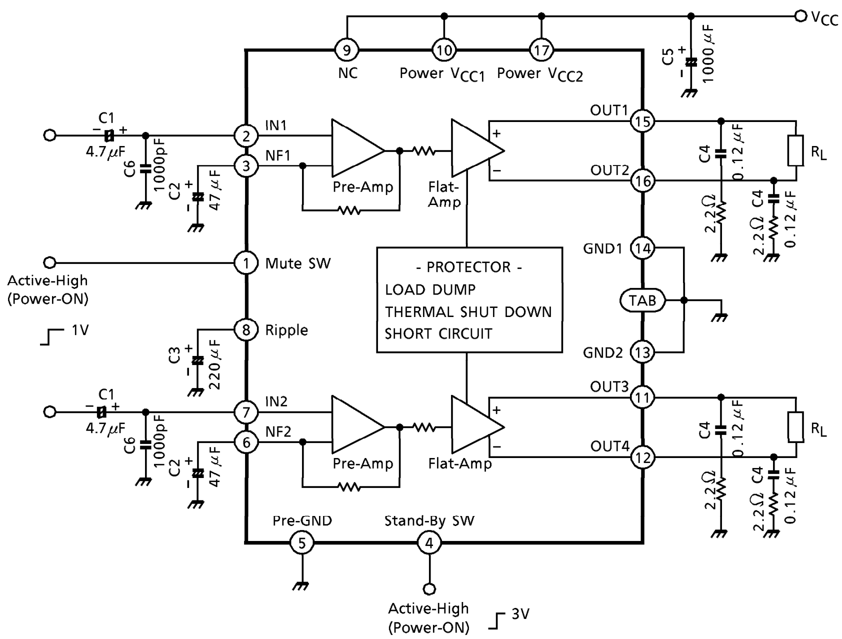
CHARACTERISTIC	SYMBOL	RATING	UNIT
Peak Supply Voltage (0.2s)	V_{CC} (surge)	50	V
DC Supply Voltage	$V_{CC (DC)}$	25	V
Operating Supply Voltage	$V_{CC (opr)}$	18	V
Output Current (Peak)	$I_{O (peak)}$	9	A
Power Dissipation	P_D	50	W
Operating Temperature	T_{opr}	−30~85	°C
Storage Temperature	T_{stg}	−55~150	°C

ELECTRICAL CHARACTERISTICS
(Unless otherwise specified, V_{CC} = 13.2V, R_L = 4Ω, f = 1kHz, Ta = 25°C)

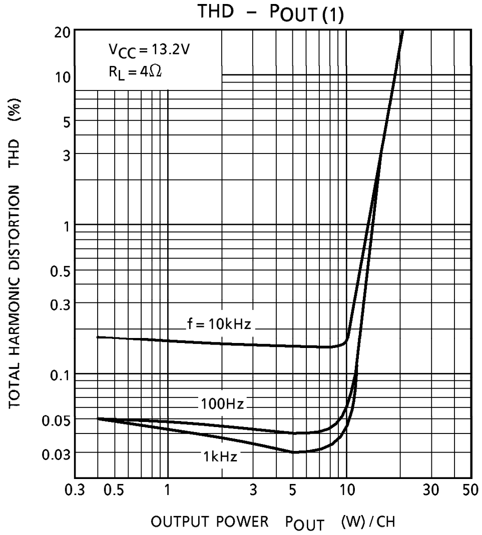
CHARACTERISTIC	SYMBOL	TEST CIR-CUIT	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Quiescent Supply Current	I_{CCQ}	—	V_{IN} = 0	—	120	250	mA
Output Power	$P_{OUT (1)}$	—	V_{CC} = 14.4V, R_L = 2Ω, THD = 10%	—	30	—	W
	$P_{OUT (2)}$	—	R_L = 2Ω, THD = 10%	17	26	—	
	$P_{OUT (3)}$	—	THD = 10%	16	19	—	
Total Harmonic Distortion Ratio	THD	—	P_{OUT} = 1W	—	0.04	0.4	%
Voltage Gain	G_V	—	—	48	50	52	dB
Voltage Gain Ratio	ΔG_V	—	—	−1.0	0	1.0	dB
Output Noise Voltage	V_{NO}	—	R_g = 0Ω, BW = 20Hz~20kHz	—	0.3	0.7	mV_{rms}
Ripple Rejection Ratio	R.R.	—	fripple = 100Hz, R_g = 600Ω	40	54	—	dB
Input Resistance	R_{IN}	—	—	—	30	—	kΩ
Output Offset Voltage	V_{offset}	—	V_{IN} = 0	−100	0	100	mV
Current at Stand-by State	I_{SB}	—	—	—	100	150	μA
Cross Talk	C.T.	—	R_g = 600Ω, V_{OUT} = 0.775V_{rms} (0dBm)	—	60	—	dB
Pin④ Control Voltage	V_{SB}	—	Stand-by→OFF (Power→ON)	2.5	—	V_{CC}	V
Pin① Control Voltage	$V_{(Mute)}$	—	Mute→ON (Power→OFF)	—	1.0	2.0	V

TEST CIRCUIT

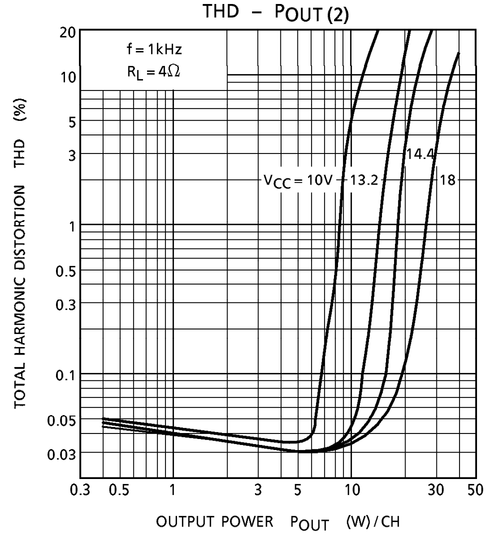
TA8221AH, TA8221AL ($G_V = 50dB$)



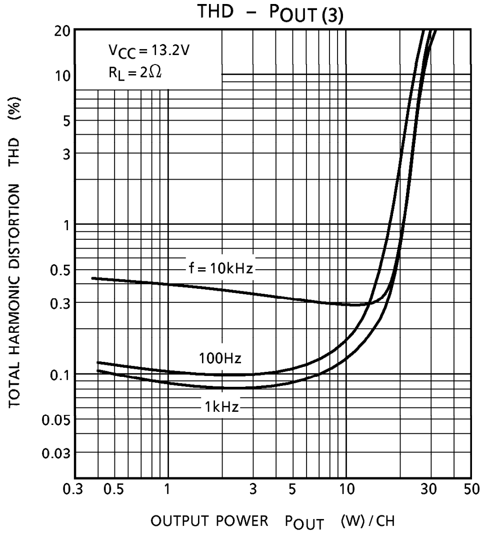
THD – POUT (1)



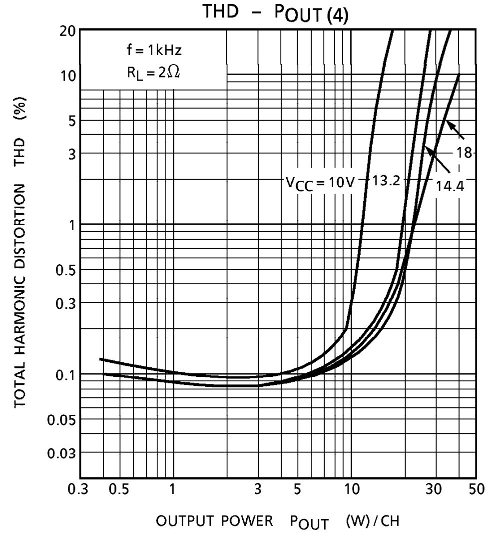
THD – POUT (2)

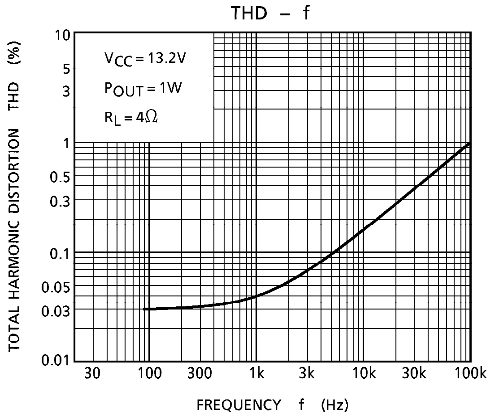


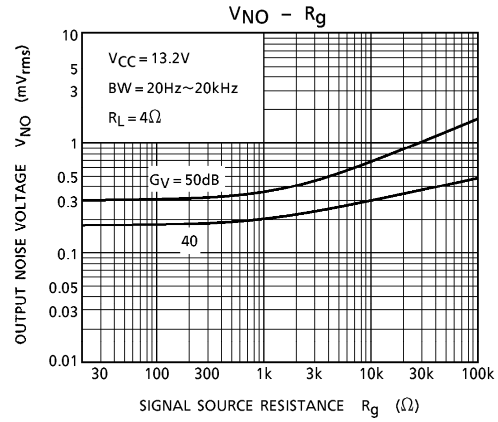
THD – POUT (3)

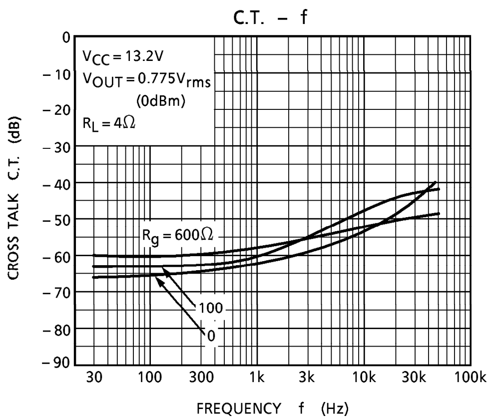


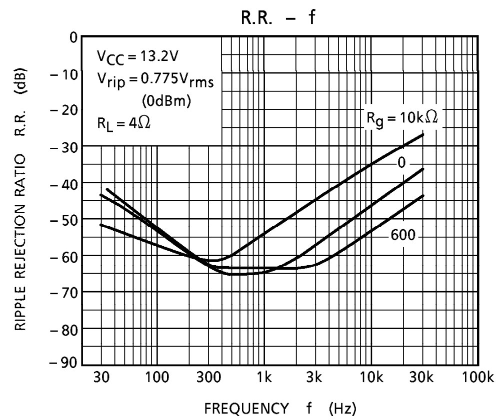
THD – POUT (4)

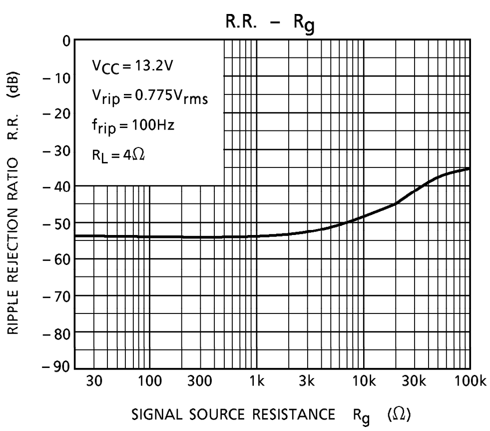


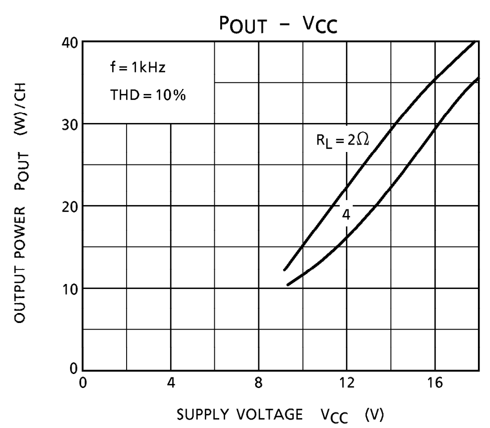

THD – f
VCC = 13.2V
POUT = 1W
RL = 4Ω
TOTAL HARMONIC DISTORTION THD (%)
FREQUENCY f (Hz)


VNO – Rg
VCC = 13.2V
BW = 20Hz~20kHz
RL = 4Ω
GV = 50dB
40
OUTPUT NOISE VOLTAGE VNO (mVrms)
SIGNAL SOURCE RESISTANCE Rg (Ω)

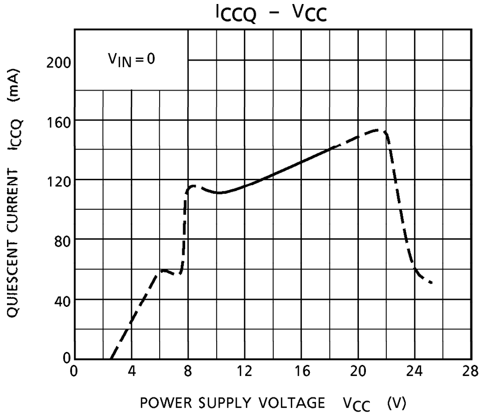

C.T. – f
VCC = 13.2V
VOUT = 0.775Vrms
(0dBm)
RL = 4Ω
Rg = 600Ω
100
0
CROSS TALK C.T. (dB)
FREQUENCY f (Hz)


R.R. – f
VCC = 13.2V
Vrip = 0.775Vrms
(0dBm)
RL = 4Ω
Rg = 10kΩ
0
600
RIPPLE REJECTION RATIO R.R. (dB)
FREQUENCY f (Hz)

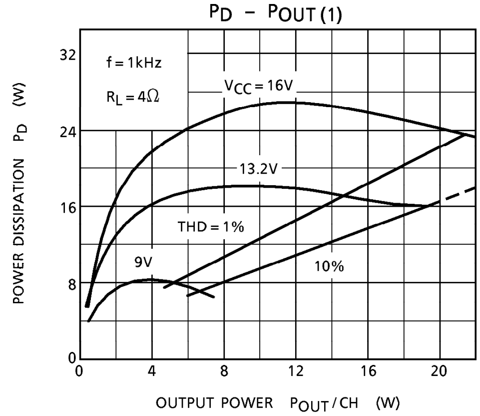

R.R. – Rg
VCC = 13.2V
Vrip = 0.775Vrms
frip = 100Hz
RL = 4Ω
RIPPLE REJECTION RATIO R.R. (dB)
SIGNAL SOURCE RESISTANCE Rg (Ω)


POUT – VCC
f = 1kHz
THD = 10%
RL = 2Ω
4
OUTPUT POWER POUT (W)/CH
SUPPLY VOLTAGE VCC (V)

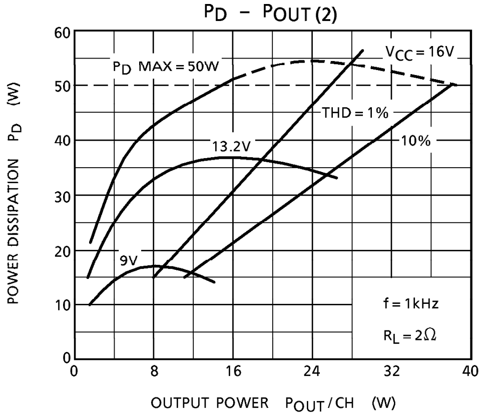
I_{CCQ} – V_{CC}



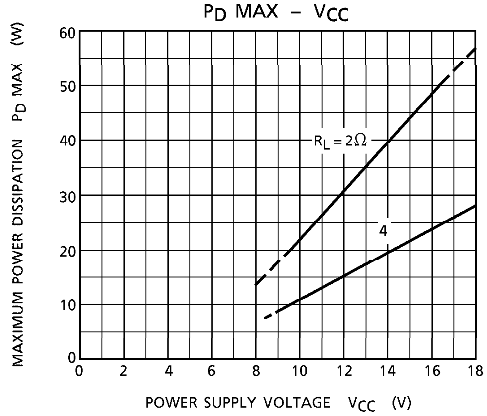
P_D – P_{OUT} (1)



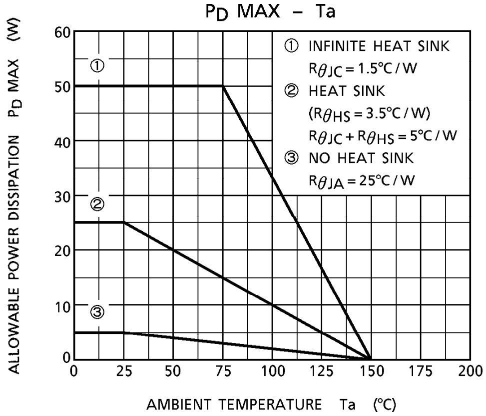
P_D – P_{OUT} (2)



P_D MAX – V_{CC}

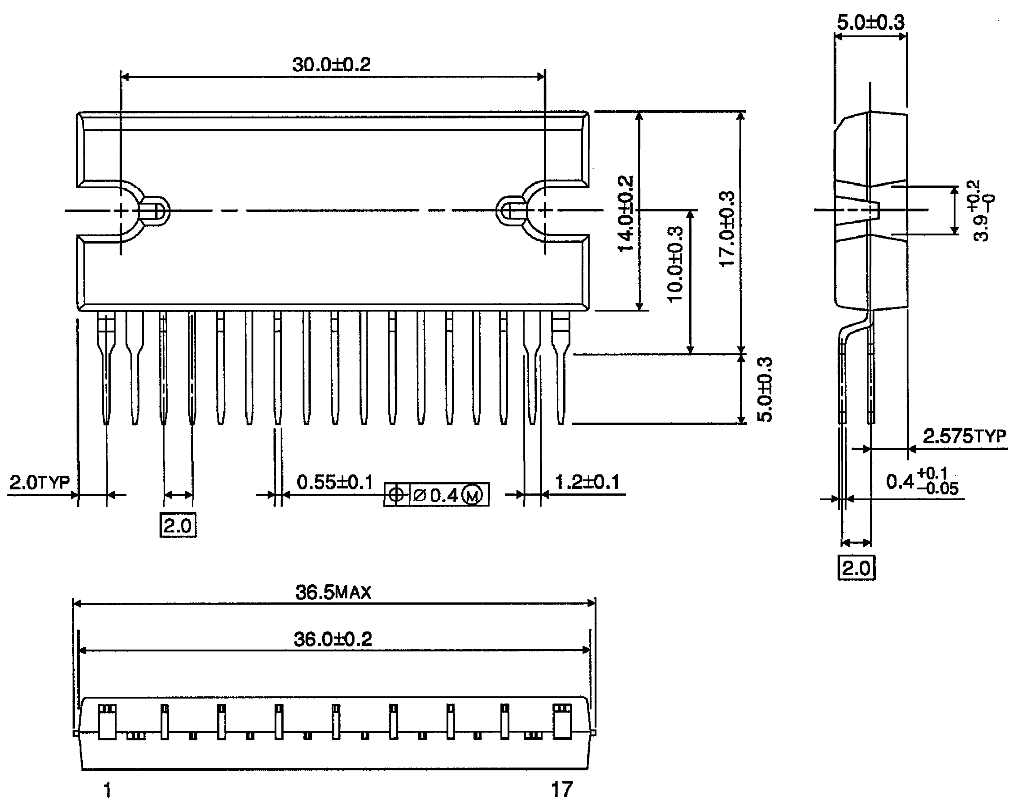


P_D MAX – Ta



OUTLINE DRAWING
HZIP17-P-2.00

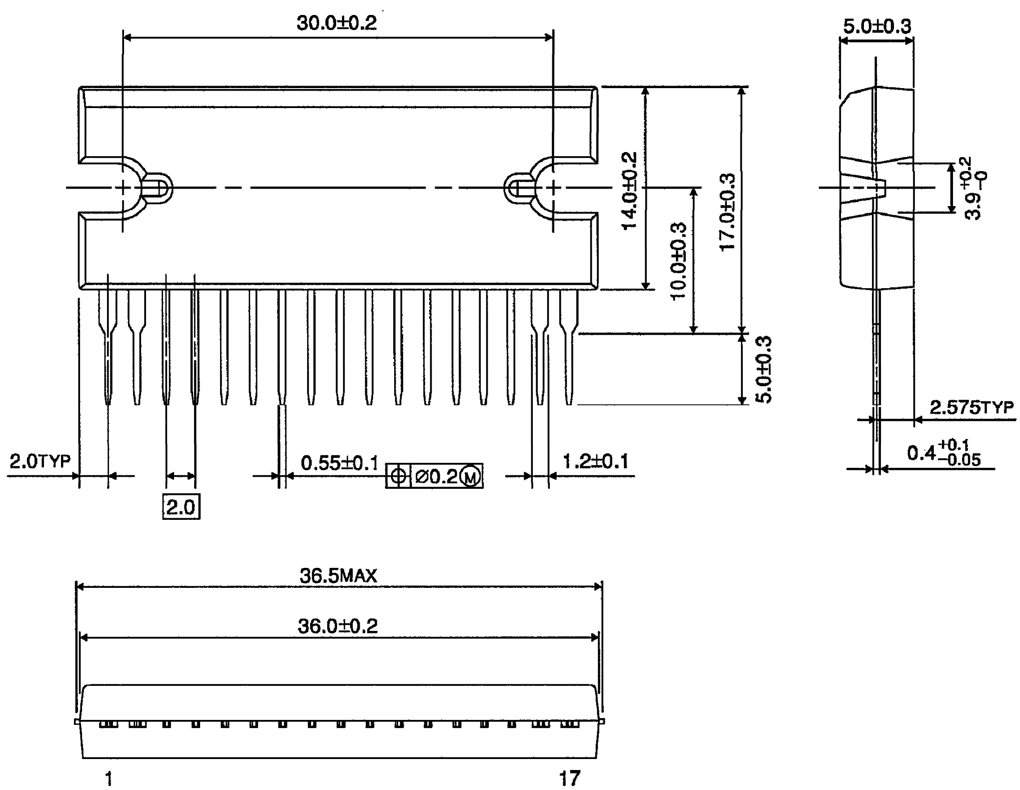
Unit : mm



Weight : 9.8g (Typ.)

OUTLINE DRAWING
HSIP17-P-2.00

Unit : mm



Weight : 9.8g (Typ.)