

**NIKO-SEM****P-Channel Logic Level Enhancement**

Mode Field Effect Transistor

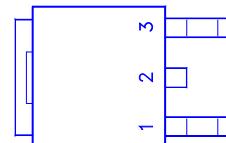
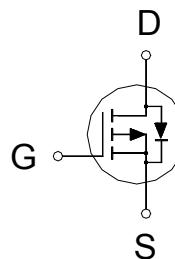
**P1604ED**

TO-252

Halogen-Free &amp; Lead-Free

**PRODUCT SUMMARY**

$V_{(BR)DSS}$	$R_{DS(ON)}$	$I_D$
-40V	16mΩ	-43A



1. GATE
2. DRAIN
3. SOURCE

**ABSOLUTE MAXIMUM RATINGS ( $T_A = 25^\circ\text{C}$  Unless Otherwise Noted)**

PARAMETERS/TEST CONDITIONS		SYMBOL	LIMITS	UNITS
Drain-Source Voltage		$V_{DS}$	-40	V
Gate-Source Voltage		$V_{GS}$	±20	V
Continuous Drain Current	$T_C = 25^\circ\text{C}$	$I_D$	-43	A
	$T_C = 70^\circ\text{C}$		-34	
Pulsed Drain Current <sup>1</sup>		$I_{DM}$	-130	
Avalanche Current		$I_{AS}$	-40.8	
Avalanche Energy	$L = 0.1\text{mH}$	$E_{AS}$	83	mJ
Power Dissipation	$T_C = 25^\circ\text{C}$	$P_D$	50	W
	$T_C = 70^\circ\text{C}$		32	
Operating Junction & Storage Temperature Range		$T_j, T_{stg}$	-55 to 150	°C

**THERMAL RESISTANCE RATINGS**

THERMAL RESISTANCE	SYMBOL	TYPICAL	MAXIMUM	UNITS
Junction-to-Case	$R_{\theta JC}$		2.5	°C / W
Junction-to-Ambient	$R_{\theta JA}$		75	°C / W

<sup>1</sup>Pulse width limited by maximum junction temperature.

**ELECTRICAL CHARACTERISTICS ( $T_J = 25^\circ\text{C}$ , Unless Otherwise Noted)**

PARAMETER	SYMBOL	TEST CONDITIONS	LIMITS			UNIT
			MIN	TYP	MAX	
<b>STATIC</b>						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0\text{V}, I_D = -250\mu\text{A}$	-40			V
Gate Threshold Voltage	$V_{GS(\text{th})}$	$V_{DS} = V_{GS}, I_D = -250\mu\text{A}$	-1.5	-2.2	-3.0	
Gate-Body Leakage	$I_{GSS}$	$V_{DS} = 0\text{V}, V_{GS} = \pm 20\text{V}$			±100	nA
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS} = -32\text{V}, V_{GS} = 0\text{V}$			1	$\mu\text{A}$
		$V_{DS} = -30\text{V}, V_{GS} = 0\text{V}, T_J = 125^\circ\text{C}$			10	
On-State Drain Current <sup>1</sup>	$I_{D(\text{ON})}$	$V_{DS} = -5\text{V}, V_{GS} = -10\text{V}$	-130			A

**NIKO-SEM****P-Channel Logic Level Enhancement**

Mode Field Effect Transistor

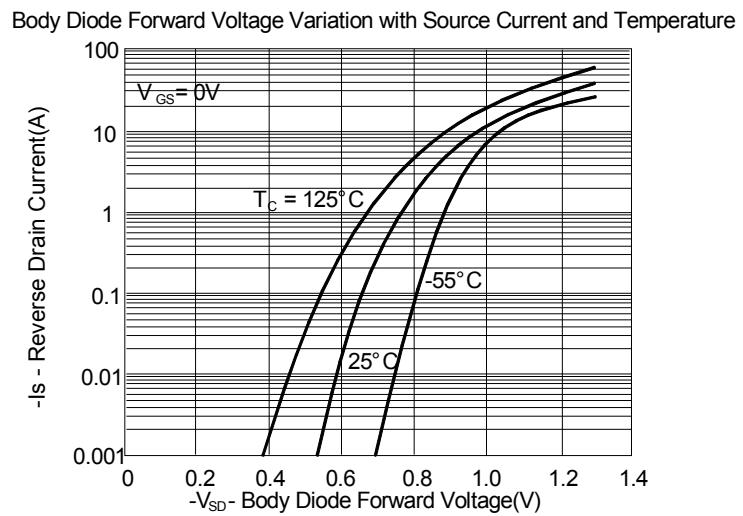
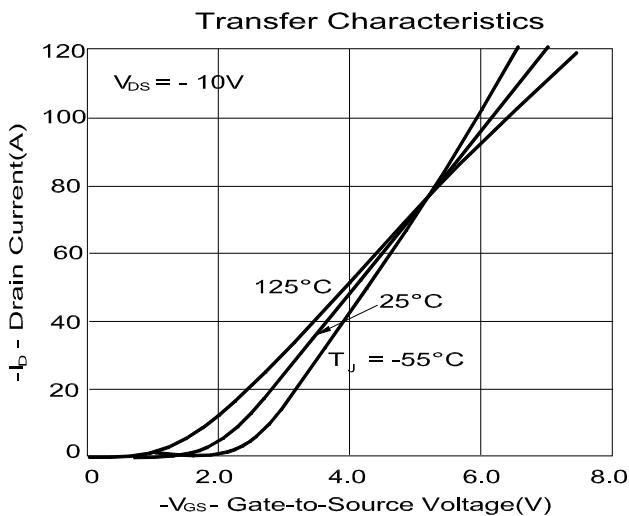
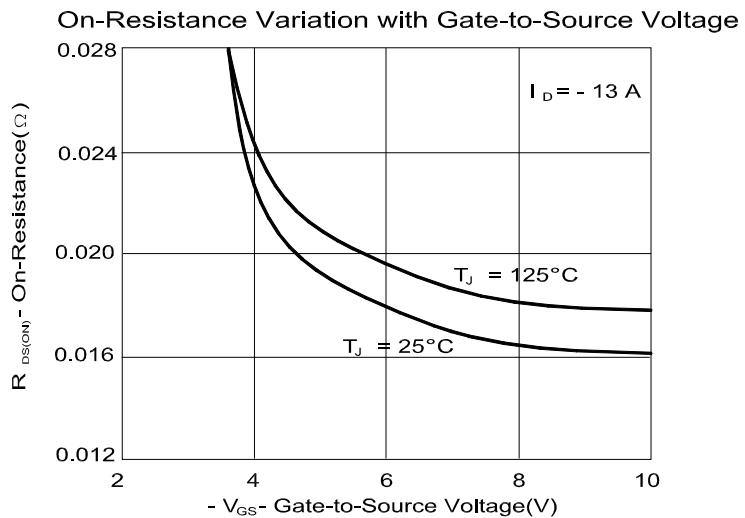
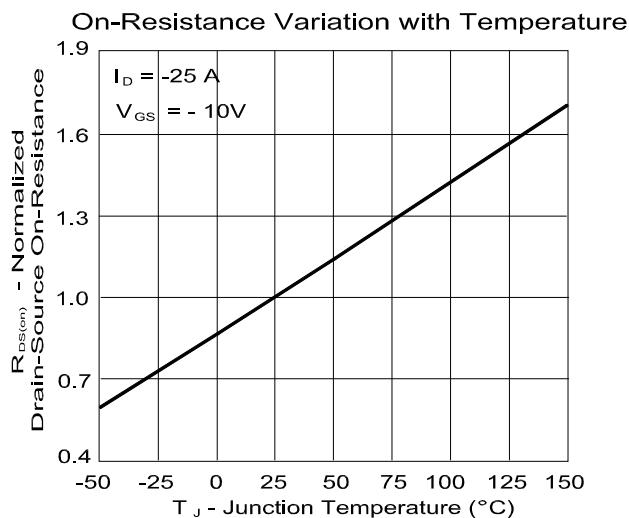
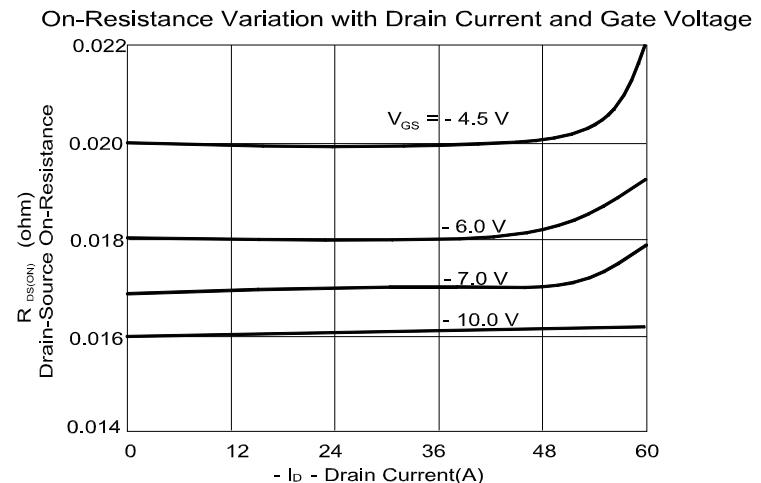
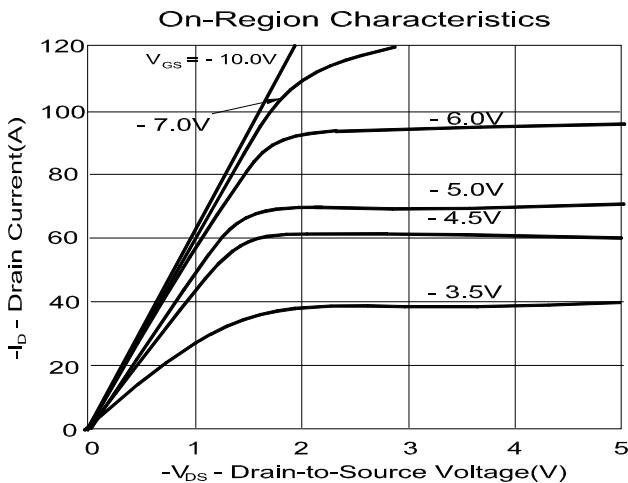
**P1604ED**

TO-252

Halogen-Free &amp; Lead-Free

Drain-Source On-State Resistance <sup>1</sup>	$R_{DS(ON)}$	$V_{GS} = -7V, I_D = -15A$		16	20	$m\Omega$
		$V_{GS} = -10V, I_D = -25A$		13	16	
Forward Transconductance <sup>1</sup>	$g_{fs}$	$V_{DS} = -5V, I_D = -25A$		24		S
<b>DYNAMIC</b>						
Input Capacitance	$C_{iss}$	$V_{GS} = 0V, V_{DS} = -15V, f = 1MHz$		2350		$pF$
Output Capacitance	$C_{oss}$			480		
Reverse Transfer Capacitance	$C_{rss}$			310		
Gate Resistance	$R_g$	$V_{GS} = 0V, V_{DS} = 0V, f = 1MHz$		4.3		$\Omega$
Total Gate Charge <sup>2</sup>	$Q_g$	$V_{DS} = 0.5V_{(BR)DSS}, V_{GS} = -10V, I_D = -25A$		42		$nC$
Gate-Source Charge <sup>2</sup>	$Q_{gs}$			9		
Gate-Drain Charge <sup>2</sup>	$Q_{gd}$			10		
Turn-On Delay Time <sup>2</sup>	$t_{d(on)}$	$V_{DS} = -20V, I_D \cong -1A, V_{GS} = -10V, R_{GS} = 6\Omega$		15		$nS$
Rise Time <sup>2</sup>	$t_r$			43		
Turn-Off Delay Time <sup>2</sup>	$t_{d(off)}$			62		
Fall Time <sup>2</sup>	$t_f$			50		
<b>SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS (<math>T_J = 25^\circ C</math>)</b>						
Continuous Current	$I_S$				-43	A
Forward Voltage <sup>1</sup>	$V_{SD}$	$I_F = -25A, V_{GS} = 0V$			-1.3	V
Reverse Recovery Time	$t_{rr}$	$I_F = -20A, dI_F/dt = 100A/\mu S$		43		$nS$
Reverse Recovery Charge	$Q_{rr}$			31		$nC$

<sup>1</sup>Pulse test : Pulse Width  $\leq 300 \mu sec$ , Duty Cycle  $\leq 2\%$ .<sup>2</sup>Independent of operating temperature.

**NIKO-SEM****P-Channel Logic Level Enhancement  
Mode Field Effect Transistor****P1604ED**  
TO-252  
**Halogen-Free & Lead-Free****TYPICAL PERFORMANCE CHARACTERISTICS**

**NIKO-SEM**

**P-Channel Logic Level Enhancement  
Mode Field Effect Transistor**

**P1604ED**

**TO-252**

**Halogen-Free & Lead-Free**

